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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

of

	Com	plete if Known	
Ap	plication Number	10/665,970	
Fili	ng Date	September 18, 2003	
First Named Inventor:		Soma et al.	
Art	Unit	2863	
Ex	aminer Name	Bryan Bui	
Att	orney Docket Number	4735.P005	

		NON PATENT LITERATURE DOCUMENTS	·	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published STENBAKKEN, G.N. et al., "Linear Error Modeling of Analog and Mixed-Signal Devices," Proc. IEEE International Test Conference, pp. 573-581, Nashville, TN, 1991. TABATABAEI, S. et al, "PLL Jitter BIST Based on Time-to-Digital Converters," IEEE Int. Mixed-Signal Testing Workshop, Montpellier, France, 2000, 12 19-945		
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Examiner Signature	· 73 - 32	Date Considered	1/6/86
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